

Prior Art

FIG. 1
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740.305-US-01

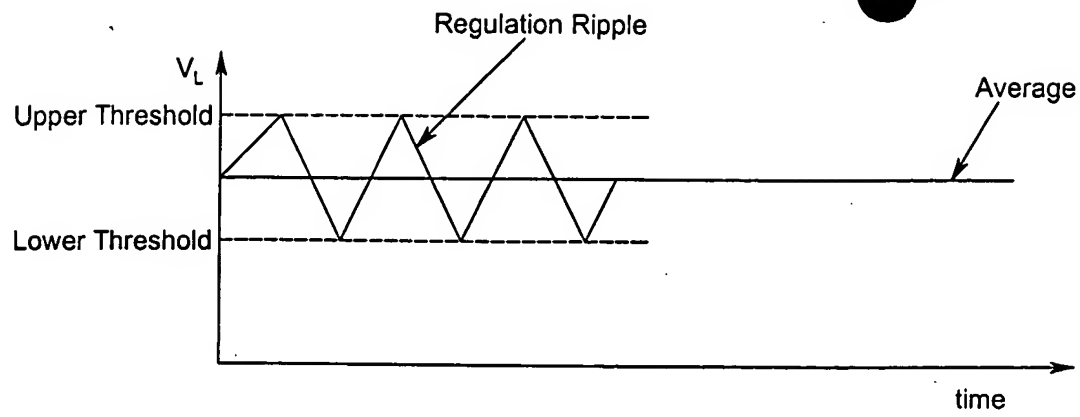


FIG. 2
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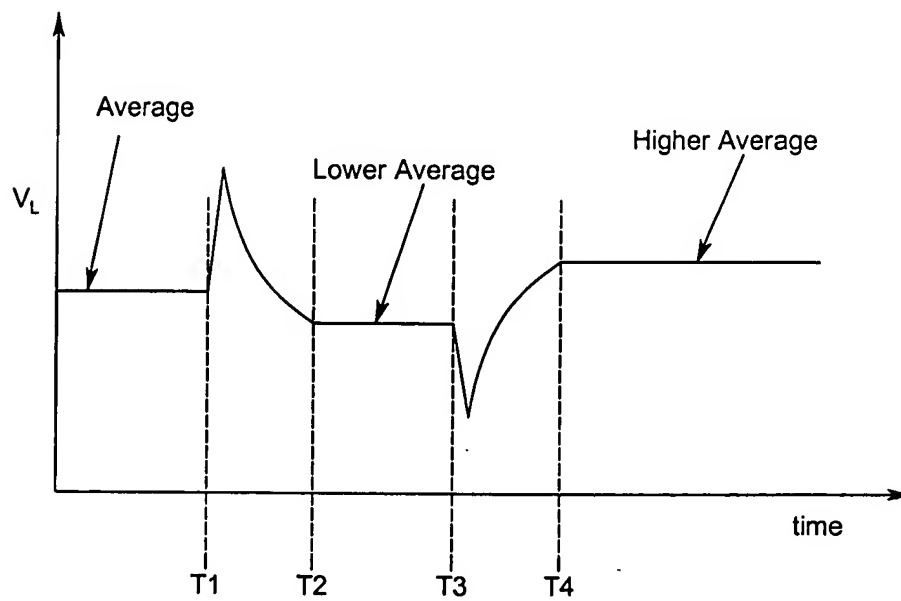
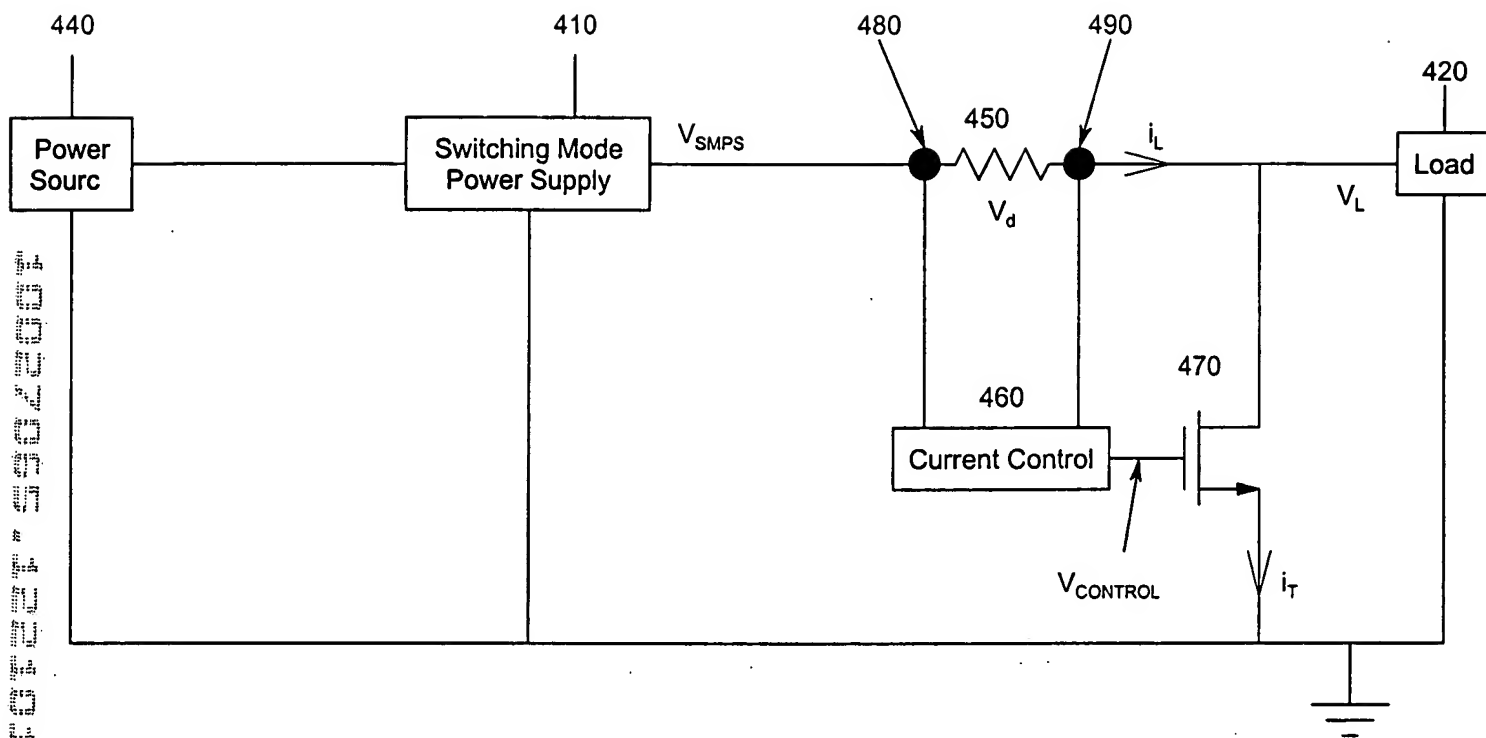


FIG. 3
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400

FIG. 4
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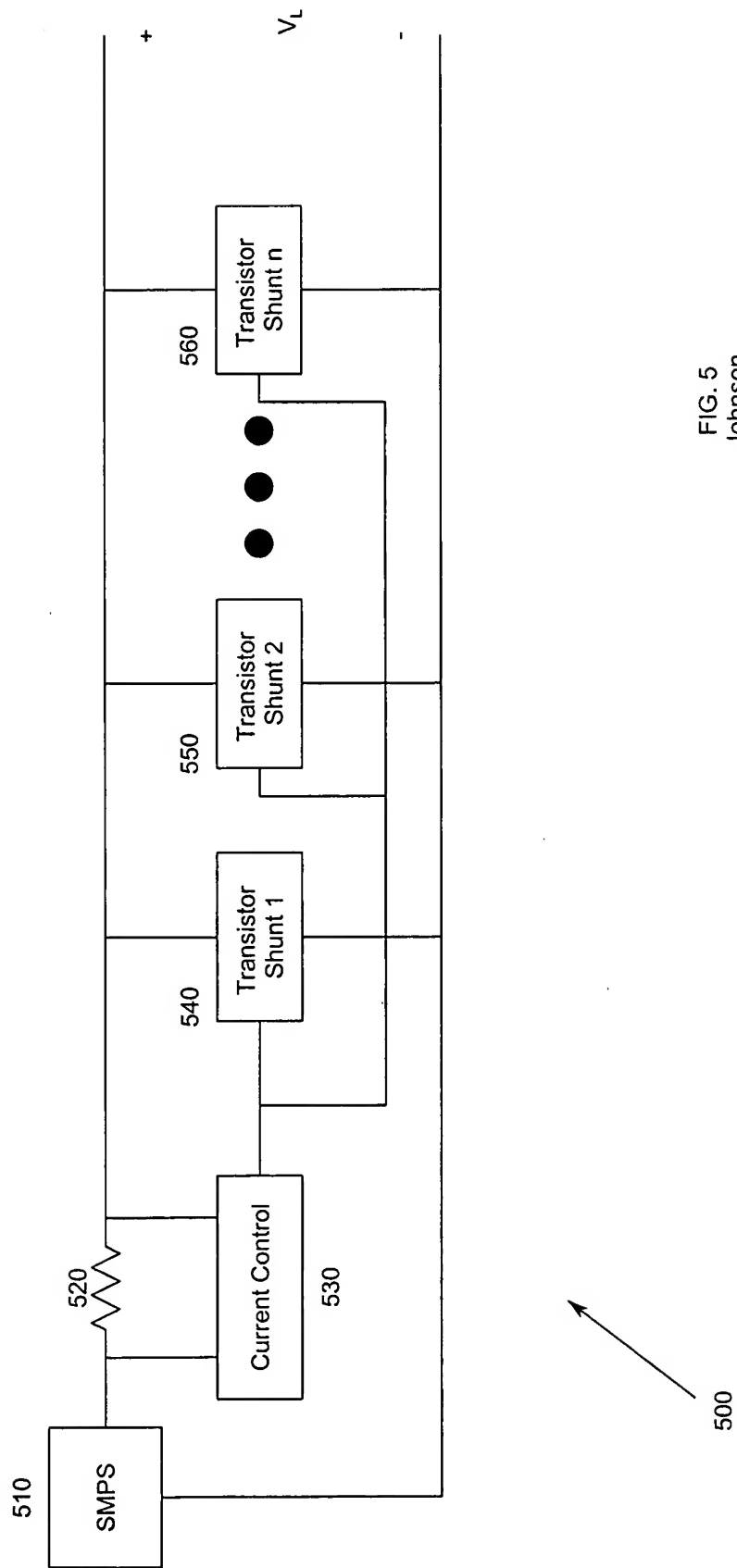
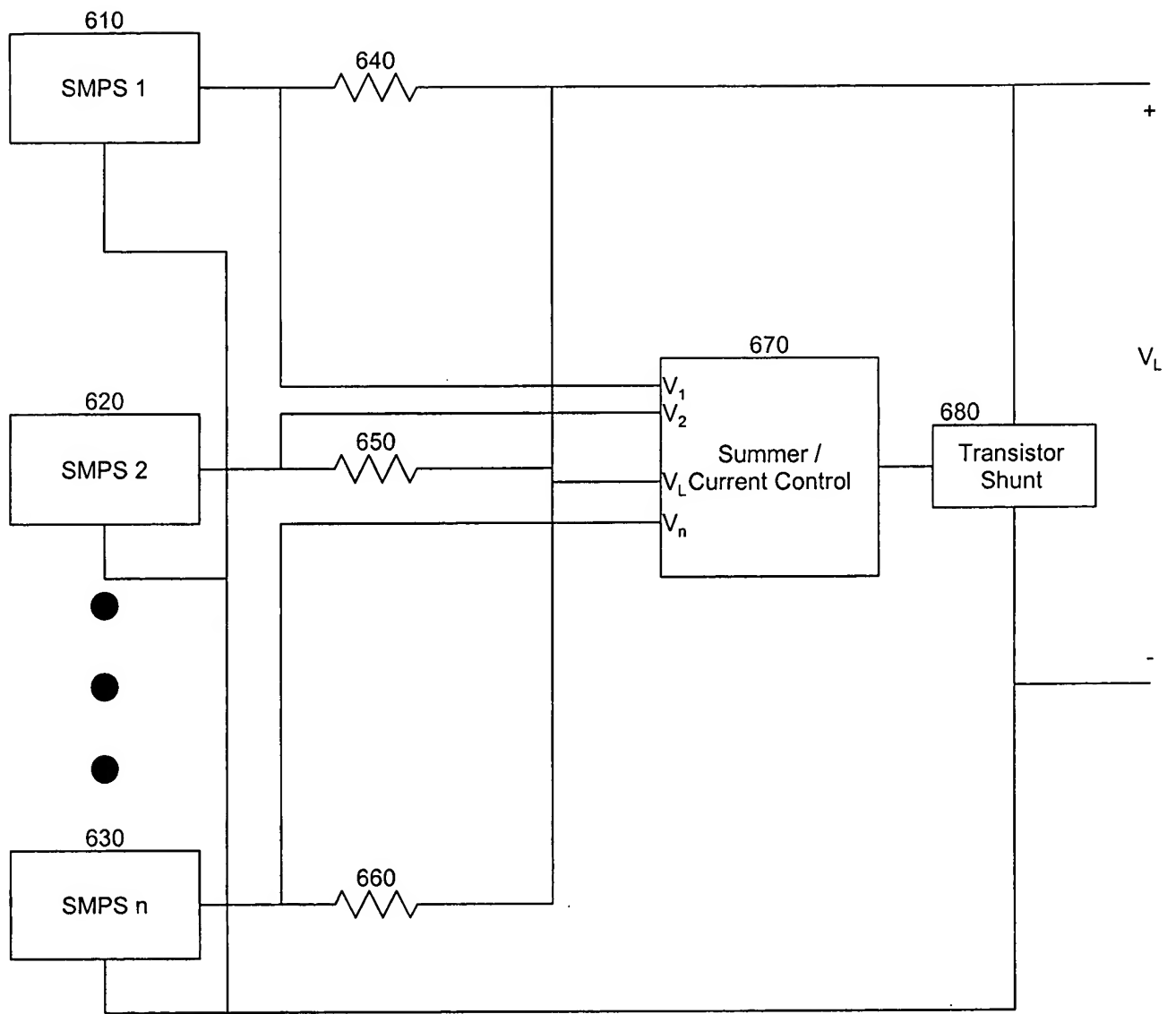


FIG. 5
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FIG. 6 is a block diagram of a system 600 for controlling the output voltage of a multi-output SMPS. The system 600 includes a plurality of SMPS units (610, 620, 630, ..., 630) and a summer/current control block (670). The output voltage of the SMPS units is sensed by resistors (640, 650, 660, ...) and the sensed voltages (V₁, V₂, V_L, V_n) are summed and used for current control. The system 600 also includes a transistor shunt (680) connected to the output of the summer/current control block (670). The output voltage is labeled V_L.



600

FIG. 6
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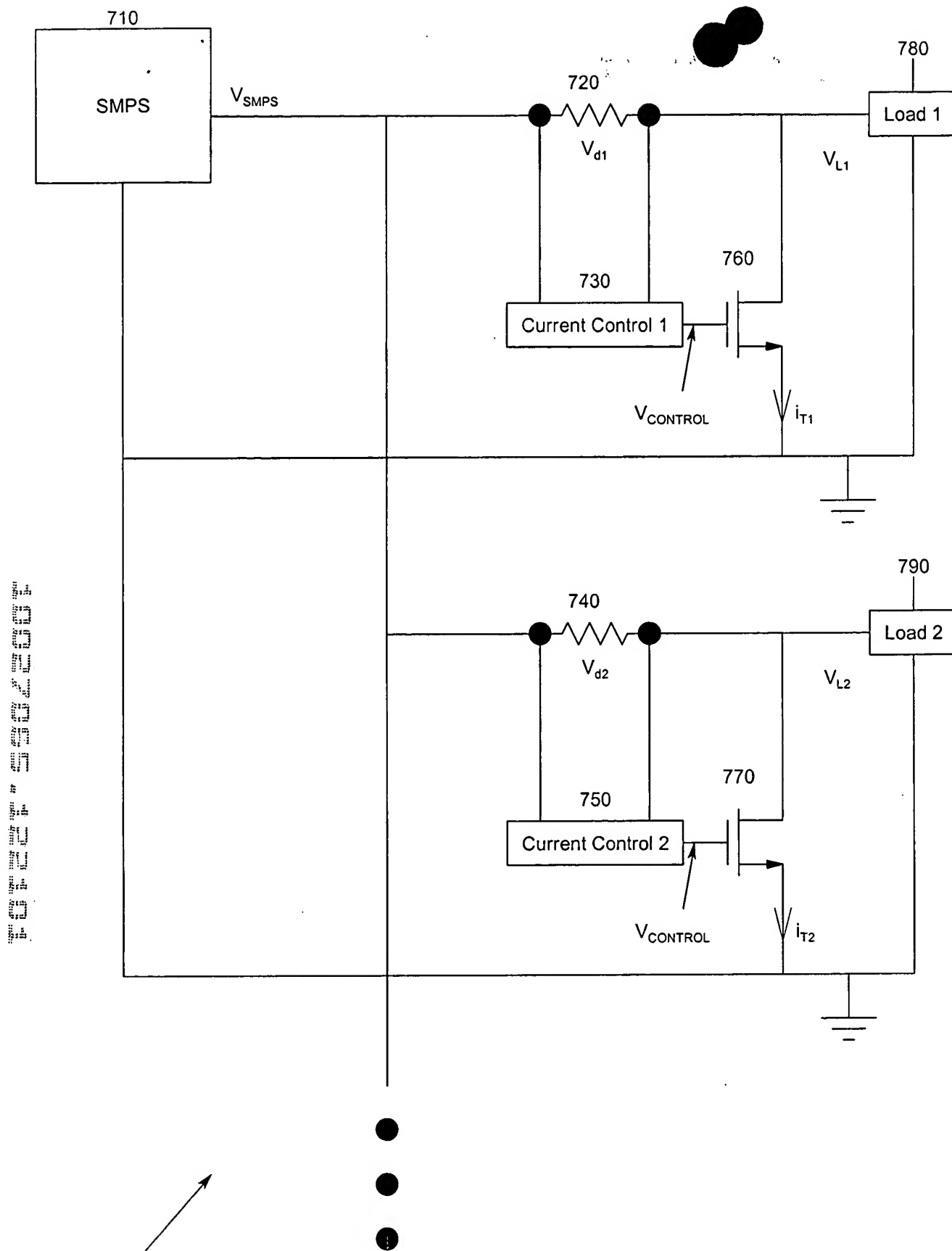


FIG. 7
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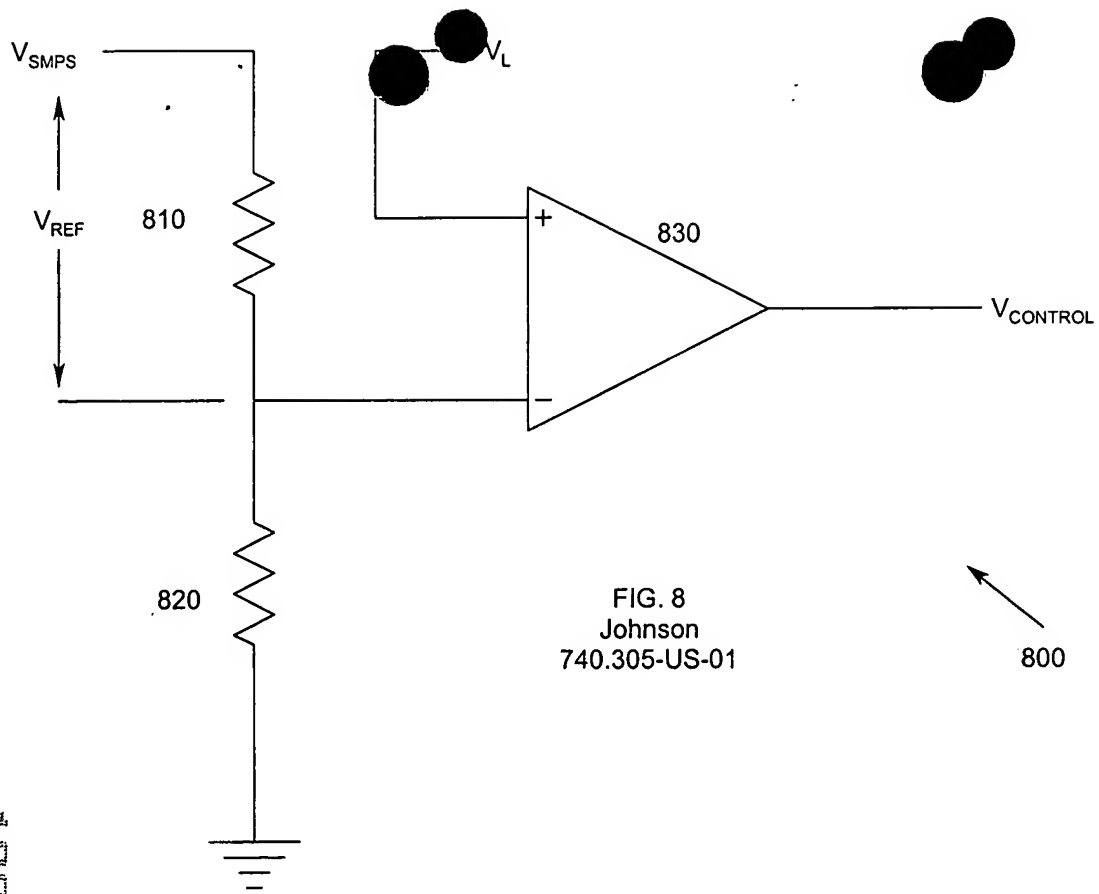


FIG. 8
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800

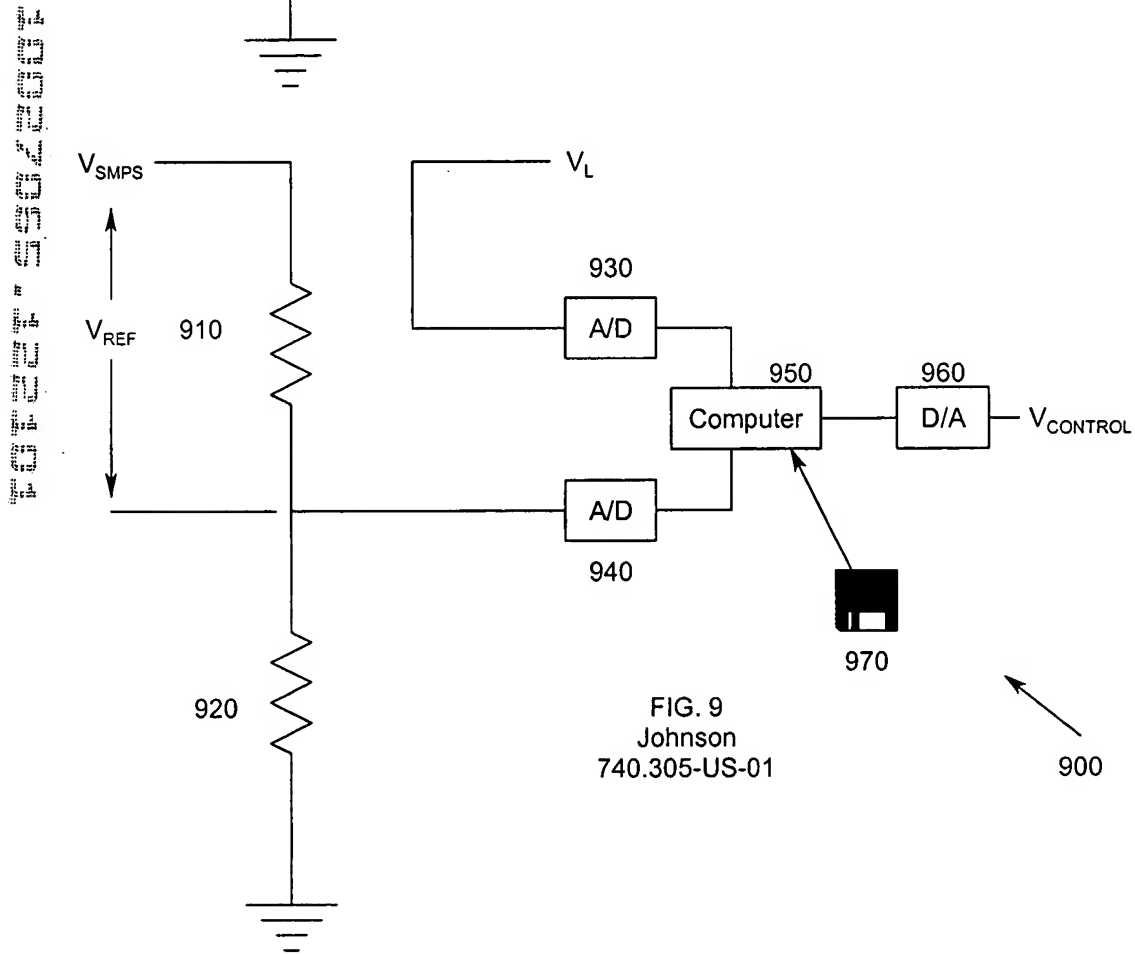


FIG. 9
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900